

Code Generation

- **Intermediate code generation:** Abstract (machine independent) code.
- **Code optimization:** Transformations to the code to improve time/space performance.
- **Final code generation:** Emitting machine instructions.

Syntax Directed Translation

Interpretation:

$E \rightarrow E_1 + E_2 \quad \{ E.val := E_1.val + E_2.val; \}$

Type Checking:

$E \rightarrow E_1 + E_2 \quad \{$
 if $E_1.type \equiv E_2.type \equiv int$
 $E.type = int;$
 else
 $E.type = float;$
 }

Code Generation via Syntax Directed Translation

Code Generation:

$E \rightarrow E_1 + E_2 \quad \{$
 $E.code = E_1.code \parallel$
 $E_2.code \parallel$
 "add")
 }

Intermediate Code

“Abstract” code generated from AST

- **Simplicity and Portability**
 - Machine independent code.
 - Enables common optimizations on intermediate code.
 - Machine-dependent code optimizations postponed to last phase.

Intermediate Forms

- **Stack machine code:**
Code for a “postfix” stack machine.
- **Two address code:**
Code of the form “add r_1, r_2 ”
- **Three address code:**
Code of the form “add $src_1, src_2, dest$ ”
Quadruples and Triples: Representations for three-address code.

Quadruples

Explicit representation of three-address code.

Example: $a := a + b * -c;$

Instr	Operation	Arg 1	Arg 2	Result
(0)	uminus	c		t_1
(1)	mult	b	t_1	t_2
(2)	add	a	t_2	t_3
(3)	move	t_3		a

Triples

Representation of three-address code with implicit destination argument.

Example: $a := a + b * -c;$

Instr	Operation	Arg 1	Arg 2
(0)	uminus	c	
(1)	mult	b	(0)
(2)	add	a	(1)
(3)	move	a	(2)

Intermediate Forms

Choice depends on convenience of further processing

- Stack code is simplest to generate for expressions.
- Quadruples are most general, permitting most optimizations including code motion.
- Triples permit optimizations such as *common subexpression elimination*, but code motion is difficult.

Generating 3-address code

$$\begin{aligned}
 E &\longrightarrow E_1 + E_2 \{ \\
 &\quad E.temp = newtemp(); \\
 &\quad E.code = E_1.code \parallel E_2.code \parallel \\
 &\quad \quad E.temp \parallel ':=' \parallel E_1.temp \parallel '+\ ' \parallel E_2.temp; \\
 &\} \\
 E &\longrightarrow int \{ \\
 &\quad E.temp = newtemp(); \\
 &\quad E.code = E.temp \parallel ':=' \parallel int.val; \\
 &\} \\
 E &\longrightarrow id \{ \\
 &\quad E.temp = id.name; \\
 &\quad E.code = "; \\
 &\}
 \end{aligned}$$

Generation of Postfix Code for Boolean Expressions

$$\begin{aligned}
 E &\longrightarrow E_1 \ \&\& \ E_2 \{ \\
 &\quad E.code = E_1.code \parallel \\
 &\quad \quad E_2.code \parallel \\
 &\quad \quad \quad gen(\text{and}) \\
 &\} \\
 E &\longrightarrow ! \ E_1 \{ \\
 &\quad E.code = E_1.code \parallel \\
 &\quad \quad \quad gen(\text{not}) \\
 &\} \\
 E &\longrightarrow \text{true} \ E.code = gen(\text{load_immed}, 1) \\
 E &\longrightarrow \text{id} \ E.code = gen(\text{load}, \text{id.addr})
 \end{aligned}$$

Code for Boolean Expressions

```

if ((p != NULL)
    && (p->next != q)) {
    ... then part
} else {
    ... else part
}

```

```

load(p);
null();
neq();
load(p);
ildc(1);
getfield();
load(q);
neq();
and();
jnz elselabel;
... then part
elselabel:
... else part

```

Shortcircuit Code

```

if ((p != NULL)
    && (p->next != q)) {
    ... then part
} else {
    ... else part
}

```

```

load(p);
null();
neq();
jnz elselabel;
load(p);
ildc(1);
getfield();
load(q);
neq();
jnz elselabel;
... then part
elselabel:
... else part

```

l- and *r*-Values

```
i := i + 1;
```

- ***l*-value**: location where the value of the expression is stored.
- ***r*-value**: actual value of the expression

Computing *l*-values

```

E → id {
    E.lval = id.name;
    E.code = '';
}
E → E1 [ E2 ] {
    E.lval = newtemp();
    E.lcode = E1.lcode || E2.code ||
    E.lval || ':' || E1.lval || '+' || E2.rval
}
E → E1 . id { // for field access
    E.lval = newtemp();
    E.lcode = E1.lcode ||
    E.lval || ':' || E1.lval || '+' || id.offset
}

```

Computing lval and rval attributes

```

E → E1 = E2 {
    E.code = E1.lcode || E2.code ||
        gen('*', E1.lval ':=' E2.rval)
    E.rval = E2.rval
}
E → E1 [ E2 ] {
    E.lval = newtemp();
    E.rval = newtemp();
    E.lcode = E1.lcode || E2.code ||
        gen(E.lval ':=' E1.lval '+', E2.rval)
    E.code = E.lcode ||
        gen(E.rval ':=' '*', E.lval)
}

```

Function Calls (Call-by-Value)

```

E → E1 ( E2, E3 ) {
    E.rval = newtemp();
    E.code = E1.code ||
        E2.code ||
        E3.code ||
        gen(push E2.rval)
        gen(push E3.rval)
        gen(call E1.rval)
        gen(pop E.rval)
}

```

Function Calls (Call-by-Reference)

```

E → E1 ( E2, E3 ) {
    E.rval = newtemp();
    E.code = E1.code ||
        E2.lcode ||
        E3.lcode ||
        gen(push E2.lval)
        gen(push E3.lval)
        gen(call E1.rval)
        gen(pop E.rval)
}

```

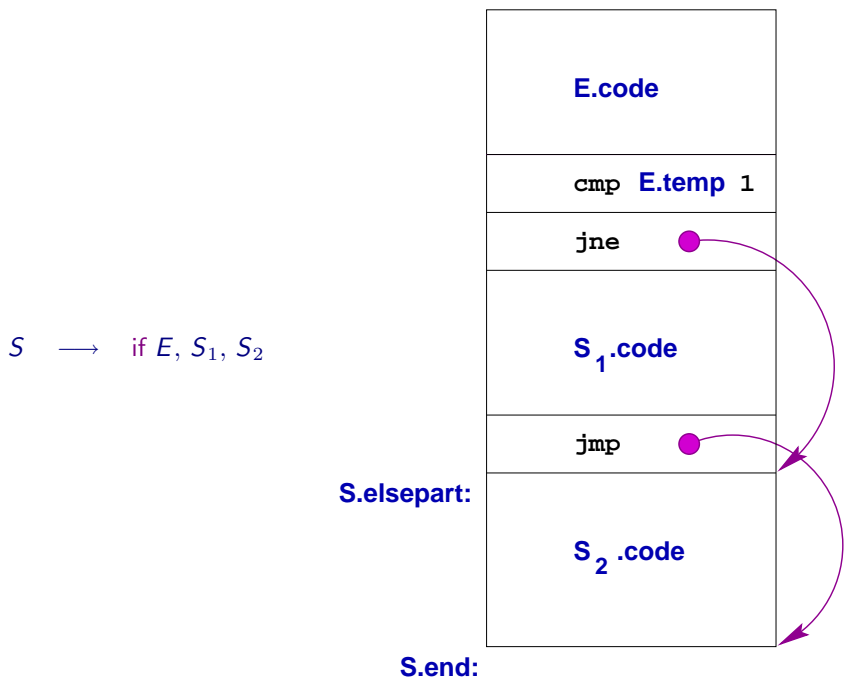
Code Generation for Statements

```

S → S1 ; S2 {
    S.code = S1.code ||
        S2.code;
}
S → E { S.code = E.code; }

```

Conditional Statements



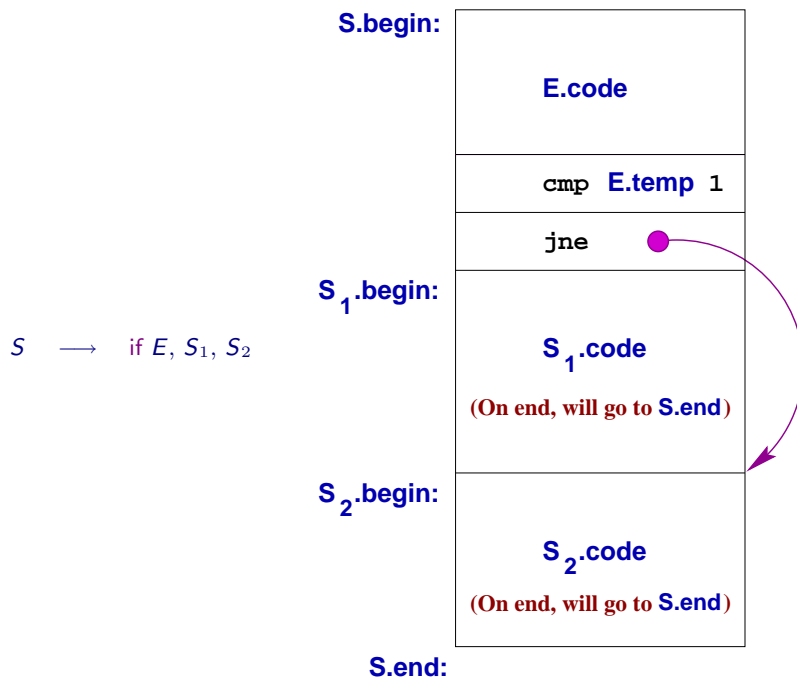
Conditional Statements

```

S → if E, S1, S2 {
    elselabel = newlabel();
    endlabel = newlabel();
    S.code =      E.code ||
                gen(cmp E.temp, 1) ||
                gen(jne elselabel) ||
                S1.code ||
                gen(jmp endlabel) ||
    gen(elselabel:) ||
                S2.code ||
    gen(endlabel:)
}

```

If Statements: An Alternative



Continuations

An attribute of a statement that specifies where control will flow to after the statement is executed.

- Analogous to the *follow* sets of grammar symbols.
- In deterministic languages, there is only one continuation for each statement.
- Can be generalized to include local variables whose values are needed to execute the following statements:

Uniformly captures *call*, *return* and *exceptions*.

Conditional Statements and Continuations

$S \longrightarrow \text{if } E, S_1, S_2$ {

```

S.begin = newlabel();
S.end = newlabel();
S1.end = S2.end = S.end;
S.code = gen(S.begin:) ||
        E.code ||
        gen(cmp E.place, 1) ||
        gen(jz S2.begin) ||
        S1.code ||
        S2.code;||
        gen(S.end:)

```

}

Continuations

- Each boolean expression has two possible continuations:
 - *E.true*: where control will go when expression in *E* evaluates to *true*.
 - *E.false*: where control will go when expression in *E* evaluates to *false*.
- Every statement *S* has one continuation, *S.next*

- Every while loop statement has an additional continuation, *S.begin*

Shortcircuit Code for Boolean Expressions

```

E  → E1 && E2 {
    E1.true = newlabel();
    E1.false = E2.false = E.false;
    E2.true = E.true;
    E.code = E1.code || gen(E1.true':') || E2.code
}
E  → E1 or E2 {
    E1.true = E2.true = E.true;
    E1.false = newlabel();
    E2.false = E.false;
    E.code = E1.code || gen(E1.false':') || E2.code
}
E  → ! E1 {
    E1.false = E.true; E1.true = E.false;
}
E  → true { E.code = gen(goto, E.true) }

```

Short-circuit code for Conditional Statements

```

S  → S1 ; S2 {
    S1.next = newlabel();
    S.code = S1.code || gen(S1.next ':') || S2.code;
}
S  → if E then S1 else S2 {
    E.true = newlabel();
    E.false = newlabel();
    S1.next = S2.next = S.next;
    S.code = E.code ||
        gen(E.true':') || S1.code ||
        gen('goto' S.next) ||
        gen(E.false':') || S2.code;
}

```

Short-circuit code for While

```

S  → while E do S1 {
    S.begin = newlabel();
    E.true = newlabel();
    E.false = S.next;
    S1.next = S.begin;
    S.code = gen(S.begin':') || E.code ||
        gen(E.true':') || S1.code ||
        gen('goto' S.begin);
}

```

Continuations and Code Generation

Continuation of a statement is an inherited attribute.

It is not an L-inherited attribute!

Code of statement is a synthesized attribute, but is dependent on its continuation.

Backpatching: Make two passes to generate code.

1. Generate code, leaving “holes” where continuation values are needed.
2. Fill these holes on the next pass.

Machine Code Generation Issues

- Register assignment
- Instruction selection
- ...

How GCC Handles Machine Code Generation

- gcc uses machine descriptions to *automatically* generate code for target machine
- machine descriptions specify:
 - memory addressing (bit, byte, word, big-endian, ...)
 - registers (how many, whether general purpose or not, ...)
 - stack layout
 - parameter passing conventions
 - semantics of instructions
 - ...

Specifying Instruction Semantics

- gcc uses intermediate code called RTL, which uses a LISP-like syntax
- after parsing, programs are translated into RTL
- semantics of each instruction is also specified using RTL:

```
movl (r3), @8(r4) ≡  
  (set (mem: SI (plus: SI (reg: SI 4) (const_int 8)))  
       (mem: SI (reg: SI 3)))
```

- cost of machine instructions also specified
- gcc code generation = selecting a low-cost instruction sequence that has the same semantics as the intermediate code